

Designed by Integrators for Integrators

Large homes or commercial applications, the CIS-CRS112-8P switch was built just for you! The ample connectivity and broad PoE support make this the perfect mid-range switch for your networking needs.



PoE Built-in

Power your access points, switches, IP cameras, and other devices without the need for additional PoE injectors or other solutions. The CIS-CRS112-8P can simultaneously handle higher voltage 48v power supplies (sold separately) and 24v power supplies, switching between them in order to meet the requirements of your PoE devices.

Layer 3 Switching

The CIS-CRS112-8P is fully managed via the web interface. The features below are just some of the options available:

- VLANs
- Bandwidth Limiting
- Port Isolation
- Port Security
- IGMP snooping
- And More

Plenty of Connectivity

Connect your devices to the 8 Gigabit Ethernet ports. Use the four SFP ports to create fiber links between your CIS routers and other switches.

Specifications

Product code	CRS112-8P
Architecture	MIPSBE
CPU	QCA8511
CPU Core count	1
CPU nominal frequency	400 MHz
Size of RAM	128 MB
Storage size	16 MB
Storage type	FLASH
Tested ambient temperature	-20 to +60C
UPC Code	711347442698

Powering

Max Power consumption	150W (10W without attachments)
PoE in	Passive PoE
PoE in input Voltage	18-57 V
Number of DC inputs	2 (DC jack, PoE in)
DC jack input Voltage	18-57 V

Ethernet

10/100/1000 Ethernet ports	8
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Fiber

SFP cages	4
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Peripherals

Serial port	RJ45
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Switching results

CIS-CRS112-8P

Mode	Configuration	64		512 byte		1518	
		kpps	Mbps	kpps	Mbps	kpps	Mbps
Switching	Non blocking Layer 2 throughput	17,857.1	9,142.9	2,819.5	11,548.9	975.3	11,844.0
Switching	Non blocking Layer 2 capacity	17,857.1	18,285.7	2,819.5	23,097.7	975.3	23,687.9
Switching	Non blocking Layer 1 throughput	17,857.1	12,000.0	2,819.5	12,000.0	975.3	12,000.0
Switching	Non blocking Layer 1 capacity	17,857.1	24,000.0	2,819.5	24,000.0	975.3	24,000.0

1. All tests are done with Xena Networks specialized test equipment (XenaBay), and done according to RFC2544 (Xena2544)
2. Max throughput is determined with 30+ second attempts with 0,1% packet loss tolerance in 64, 512, 1518 byte packet sizes
3. Values in *italic* indicate that max throughput was reached without maxing out CPU, but because board interface configuration was maxed out
4. Test results show device maximum performance, and are reached using mentioned hardware and software configuration, different configurations most likely will result in lower results