

Designed by Integrators for Integrators

The CIS-WAP provides the Wi-Fi coverage you need in a sleek, compact package. The weatherproof enclosure makes the CIS-WAP perfect for both indoor and outdoor environments.

Cost Effective 2.4 GHz Networking

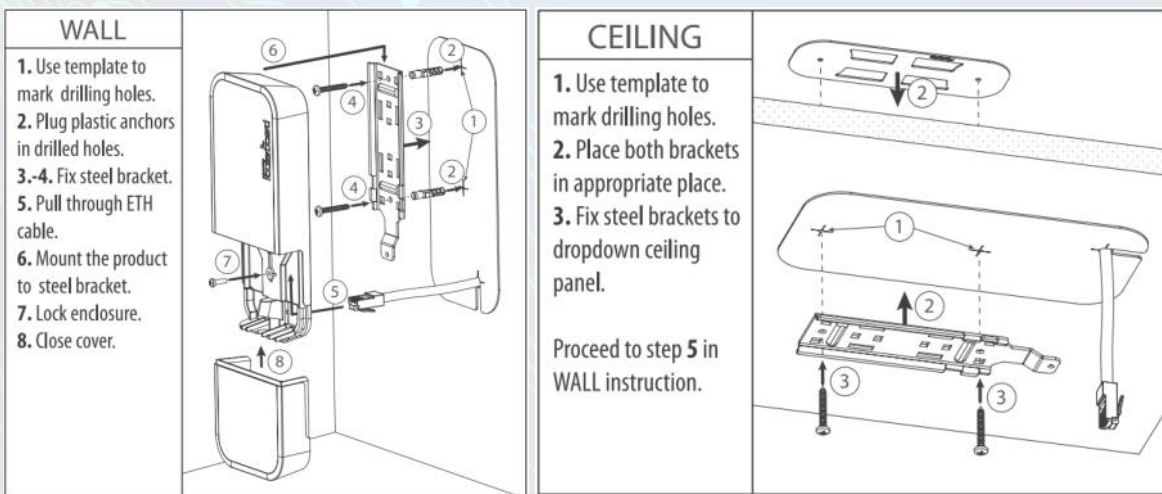
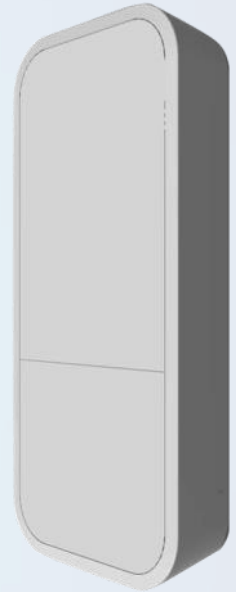
The CIS-WAP provides you with the coverage you need for your 802.11b/g/n devices.

Centralized Management

Manage all of your connected access points from a single intuitive interface with the CIS Wi-Fi manager. Included in all CIS routers, easily view the connected devices, and change passwords and SSIDs globally from your CIS router! There is no limit to the number of access points that can be managed and controlled.

Multiple Mounting Options

The access point can be mounted on any internal or external wall or ceiling. You can also mount it to a pole using the included plastic straps. The cable can be hidden completely when ran inside of a wall or ceiling.



Specifications

Product code	CIS-WAP
Architecture	MIPSBE
CPU	QCA9533
CPU core count	1
CPU nominal frequency	650 MHz
Dimensions	185 x 85 x 30 mm
Size of RAM	64 MB
Storage size	16 MB
Storage type	FLASH
Tested ambient temperature	-40C to +70C
UPC Code	711347442629

Powering

Max Power consumption	4W
PoE in	802.3af/at
PoE in input Voltage	11-57 V
Number of DC inputs	2 (DC jack, PoE-IN)
DC jack input Voltage	11-57 V

Wireless

Wireless 2.4 GHz number of chains	2
Wireless 2.4 GHz standards	802.11b/g/n
Antenna gain dBi for 2.4 GHz	2
Wireless 2.4 GHz chip model	QCA9533

Ethernet

10/100 Ethernet ports	1
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Ethernet test results

CIS-WAP		QCA9533 100M one port test					
Mode	Configuration	1518 byte		512 byte		64 byte	
		kpps	Mbps	kpps	Mbps	kpps	Mbps
Bridging	none (fast path)	8.1	98.4	23.5	96.3	173.6	88.9
Bridging	25 bridge filter rules	8.1	98.4	23.5	96.3	70.9	36.3
Routing	none (fast path)	8.1	98.4	23.5	96.3	160.8	82.3
Routing	25 simple queues	8.1	98.4	23.5	96.3	90.8	46.5
Routing	25 ip filter rules	8.1	98.4	23.5	96.3	43.2	22.1

1. All tests are done with Xena Networks specialized test equipment (XenaBay), and done according to RFC2544 (Xena2544)
2. Max throughput is determined with 30+ second attempts with 0,1% packet loss tolerance in 64, 512, 1518 byte packet sizes
3. Values in Italic indicate that max throughput was reached without maxing out CPU, but because board interface configuration was maxed out
4. Test results show device maximum performance, and are reached using mentioned hardware and software configuration, different configurations most likely will result in lower results